Yavuz Korman

+447541991352 | Email: yavuz.korman@gmail.com| LinkedIn: www.linkedin.com/in/yavuzkorman

STATEMENT

Power Systems Engineer working at ARUP. CIGRE UK NGN Committee Membership Team Lead. Experienced in Power System Analysis, Cable Analysis, Protection Coordination and Selectivity studies, Arc Flash Studies. Active role in client relations. Experienced with National Grid specifications and projects, and datacentre projects. First-Class Honours MSc Power Systems graduate of UCL. First-Class Honours engineering graduate of The University of Edinburgh. EU citizen with UK settled status. Very fluent in English, and native Turkish speaker. Strengths include good leadership skills, problem-solving, organisational, and team-working skills, which I have developed in my work experience. Very confident in SKM PTW, ETAP, MATLAB, PowerFactory DIgSILENT, CYMCAP, CDEGS, C programming language.

EDUCATION AND QUALIFICATIONS

UCL (University College London), ***MSc Power Systems Engineering*** *(London, UK)*

- **GPA:** A (First-Class Honours)
- **Key Modules:** Power Transmission, Electrical Machines, Power Electronic Drives, Electrical Propulsion, Auxiliary Machinery Systems, New and Renewable Energy Systems, Project Management.

09/2021 - Present

The University of Edinburgh, ***BEng (Hons) Electronic and Electrical Engineering*** *(Edinburgh, UK)*

- **GPA:** A (First-Class Honours)
- **Key Modules:** Power Electronics, Machines and Systems, Analogue and Digital Systems Design**,** Signals and Communication Systems, Engineering Software (Cadence, C, Verilog), Microelectronics.

09/2017 – 06/2021

Turk Maarif College, ***GCE International Advanced Level*** (*Nicosia, Cyprus)*

- **GPA:** 9.81/10 (Achieved Certificate of Merit).
- Physics A*, Mathematics A, Chemistry A, Turkish A.

09/2013 – 06/2017

ENGINEERING WORK EXPERIENCE

ARUP ***Power Systems Engineer*** (*Birmingham, UK)*

- Power Systems Analysis – Worked on system analysis of several datacentres. Carried out load flow and short circuit analysis with ETAP, SKM PTW, and DIgSILENT. Analysed TCCs and proposed protection settings. Worked on HV power networks, experienced with relays, transformer specifications, UPS and generators.
- Arc Flash Studies – Modelled electrical network and carried out Arc Flash simulations for numerous datacentres under various operational scenarios according to IEEE1584, produced PPE labels accordingly with NFPA 70E. Currently in the role of leading a similar project, oversee and review and coordinate work as well as client meetings.
- Cable Analysis – Electrical assessment of cables up to 400kV under several configurations using CYMCAP software. Experienced with design considerations and HV cable installation methods (ducts, direct buried, HDD). Experienced with National Grid technical specifications.
- Earthing Studies – Design of Earth grid for substations, Touch voltage, Step voltage, Earth Potential Rise simulations and calculations using CDEGS software.

09/2022-Ongoing

Slovak University of Technology (STU), ***Research Intern*** (*Bratislava, Slovakia)*

- Worked on the sustainability and economic viability of microgrids under the supervision of the director of the Institute of Power Engineering.
- Designed and analysed specific microgrids, and developed a tool to assess any microgrid's performance.
- Worked on the comprehensive analysis of renewable energy sources and

06/2019-07/2019

the consequent design of a sustainable energy system at the STU.

Hyperloop Team Member (HYPED) – The University of Edinburgh
(Edinburgh, UK)

- Design and manufacture a hyperloop pod and join the pod competition sponsored by SpaceX in California, US.
- Under the "Levitation" sub-team as a first-year student, researched and designed the pod's lower chassis under certain specifications with a colleague by using SOLIDWORKS CAD software.
- In 2018, HYPED passed all the mechanical tests and achieved a top-six position in the world at the SpaceX Hyperloop Pod Competition III.

09/2017-05/2018

Çözüm Mühendislik (Cozum Engineering Firm), *Intern*
(Nicosia, Cyprus)

- Worked as a member of a team comprising ten electrical engineers and project designers.
- Assigned to observe in various projects including installation of a solar energy-farm and smart home automation systems.
- This experience gave me a good insight into electrical engineers' working environment and the projects they undertake.

Summer 2016

FINANCE WORK EXPERIENCE

Reha Milk Factory, *Intern*
(*Nicosia, Cyprus)*

- Worked in the finance department of the factory. This has helped me see a fully functional factory's running operations, costs, and profits.
- Used the MS Excel software extensively.
  Interacted with the maintenance crew and the workers of the factory. Consequently, developed fine relations with the staff and improved my social skills.

Summer 2017

SKILLS

- **Team-working**
  Needed to collaborate effectively with my colleagues in all my work experiences to carry out and finish the tasks efficiently. Luckily, absorbed all the professional team working when I was an intern at the Cozum Engineering Firm. My latter experiences then helped me further practise and improve the art of successful cooperation within student-run project groups and professional bodies.
- **Problem Solving**
  As an intern at the STU, found solutions to complex problems through a series of research. In the hyperloop team, came up with design solutions to meet specific criteria.
- **Analytical thinker**
  Investigated renewable energy resources and batteries suitable for microgrids as an intern at the STU. Analysed unique solutions for specific microgrids. As an intern at the factory, proposed new approaches to traditional calculation methods.
- **Good communicator**
  Prepared PowerPoint presentations and showed the work I had done to my supervisors regularly in all my internships. Answered technical questions about my work. At the factory, communicated with the staff regularly and got a unique perspective from all classes of employees.

SOFTWARE

Proficient in ETAP, SKM PTW, MATLAB, CYMCAP, CDEGS, PSCAD, PowerFactory DIgSILENT, C programming language, Cadence, Verilog, Simulink, LTSpice, Microsoft Office (PowerPoint, Word, and Excel), LaTeX, R programming language.

REFERENCES

Prof. Rebecca Cheung
Institute of Integrated Micro and Nano Systems (The University of Edinburgh)
R.Cheung@ed.ac.uk

Dr. Majid Safari
Institute of Digital Communications (The University of Edinburgh)
Majid.Safari@ed.ac.uk

Prof. Ing. Frantisek Janicek
Director of the Institute of Power and Applied Electrical Engineering (STU)
frantisek.janicek@stuba.sk