

Voltage Support using Grid Forming Inverter

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Part 1 Outline

- High IBR Integration- Voltage Issues
- Grid Connected Inverters
- Literature Review
- LVRT- Low Voltage Right Through

Part 2



High IBR Integration- Voltage Issues

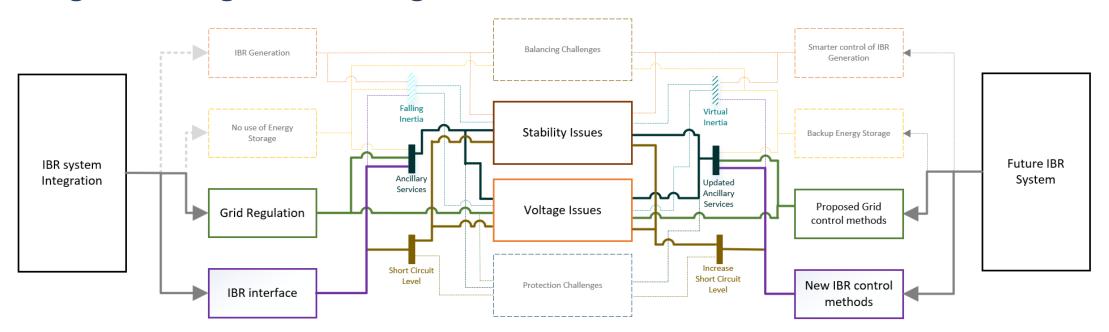


Figure 1. Voltage and Stability Implications.



System Model

- Grid Forming Inverter operates as a voltage source
- Reaching maximum capacity became uncontrollable voltage source not aligned with dq-frame
- Optimal Voltage Support has been used for improved voltage profile during a fault

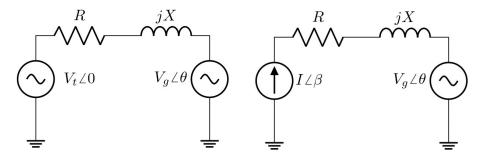


Figure 2. Independent voltage source

Figure 3. Current source with saturated current

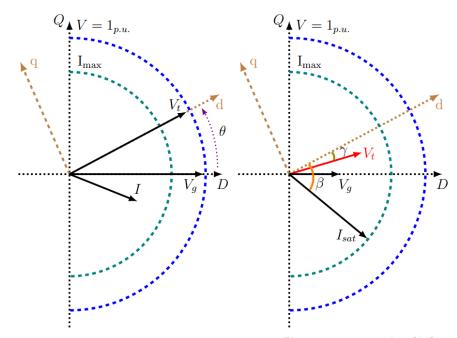


Figure 4. Phasors on saturated mode

Figure 5. Phasors using OVS



Inverter Schematic and Grid Connection Parameters

Table 1. System Parameters

Parameters	Value
X/R	2 (UG)
V_n	33kV/132kV
$I_{m\alpha x}$	1 p.u

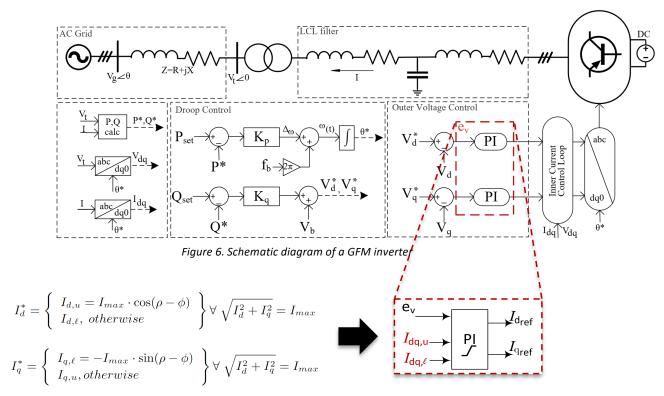


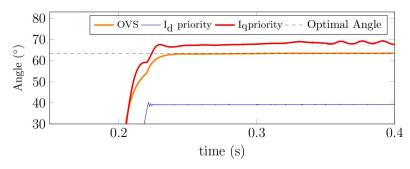
Figure 7. PI limitation technique



Suboptimal-Optimal Voltage Support

Impact on:

- Frequency restoration
- Angle stability
- Power quality issues
- Post fault spikes
- Grid code cannot be satisfied



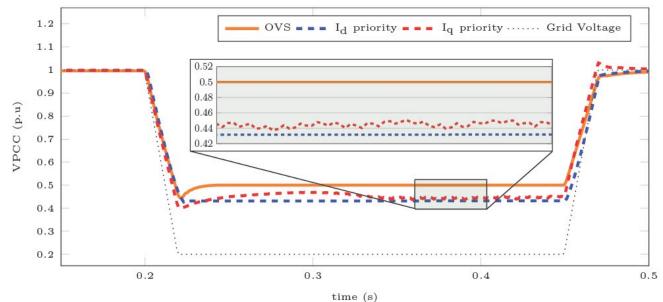


Figure 10. Voltage with Iq and Id priority and OVS.